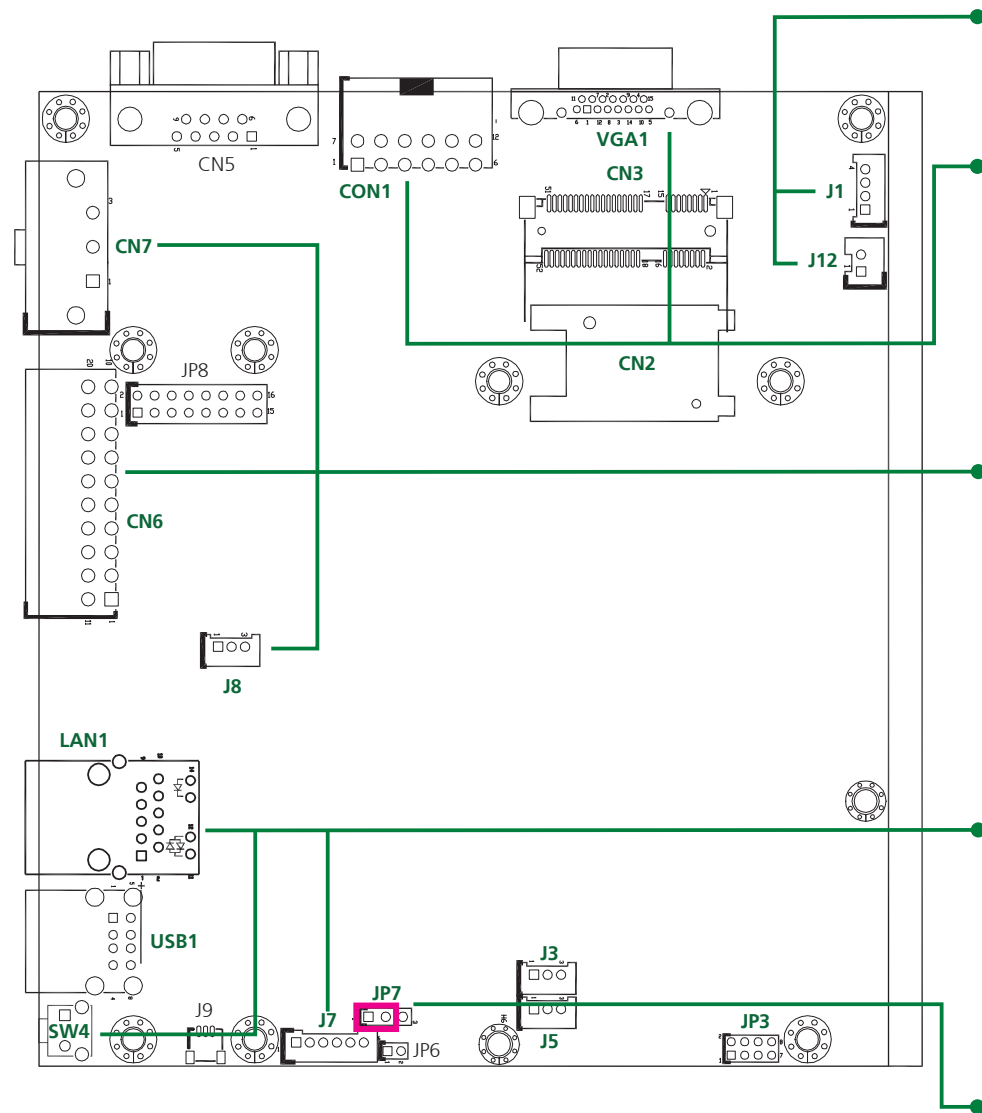


# NISB 60 Quick Reference Guide

Ver A. (P/N: 60177A0316X00)



**J1: I2C CON**

| Pin | Definition      | Pin | Definition      |
|-----|-----------------|-----|-----------------|
| 1   | GND             | 2   | VCC3            |
| 3   | AM335X_I2C0_SCL | 4   | AM335X_I2C0_SDA |

**J12: PMIC POWER ON BUTTON**

| Pin | Definition |
|-----|------------|
| 1   | PONOFF     |
| 2   | GND        |

**VGA1: VGA CON**

| Pin | Definition   | Pin | Definition    |
|-----|--------------|-----|---------------|
| 1   | RED_VGA      | 2   | GREEN_VGA     |
| 3   | BLUE_VGA     | 4   | N/A           |
| 5   | GND          | 6   | VGADET        |
| 7   | GND          | 8   | GND           |
| 9   | VCC          | 10  | GND           |
| 11  | N/A          | 12  | CH_DAC_DDCDAT |
| 13  | HSYNC_VGA    | 14  | VSYNC_VGA     |
| 15  | CH_DAC_DDCLK |     |               |

**CON1: DIO CON**

| Pin | Definition | Pin | Definition |
|-----|------------|-----|------------|
| 1   | GND        | 2   | DI1        |
| 3   | DI2        | 4   | DI3        |
| 5   | DI4        | 6   | COM1       |
| 7   | GND        | 8   | DO1        |
| 9   | DO2        | 10  | DO3        |
| 11  | DO4        | 12  | GND        |

**CN6: COM1 & COM2 CON**

| Pin | 232 Define | 422/485 Define | Pin | 232 Define | 422/485 Define |
|-----|------------|----------------|-----|------------|----------------|
| 1   | GND        | GND            | 2   | SP1_TXD    | SP1_RX1P       |
| 3   | N/A        | SP1_RX1N       | 4   | SP1_RXD    | SP1_TX1P       |
| 5   | N/A        | SP1_TX1N       | 6   | GND        | GND            |
| 7   | SP1_CTS    | SP1_RX2P       | 8   | N/A        | SP1_RX2N       |
| 9   | SP1_RTS    | SP1_TX2P       | 10  | N/A        | SP1_TX2N       |
| 11  | GND        | GND            | 12  | SP2_TXD    | SP2_RX1P       |
| 13  | N/A        | SP2_RX1N       | 14  | SP2_RXD    | SP2_TX1P       |
| 15  | N/A        | SP2_TX1N       | 16  | GND        | GND            |
| 17  | SP2_CTS    | SP2_RX2P       | 18  | N/A        | SP2_RX2N       |
| 19  | SP2_RTS    | SP2_TX2P       | 20  | N/A        | SP2_TX2N       |

**J8: COM3 CON**

| Pin | Definition |
|-----|------------|
| 1   | COM3_TXD   |
| 2   | COM3_RXD   |
| 3   | ISO_GND    |

**CN7: DC Jack**

| Pin | Definition   |
|-----|--------------|
| 1   | VAIN-        |
| 2   | VAIN+        |
| 3   | REAR_MTH_GND |

**LAN1: LAN CON**

| Pin | Definition       | Pin | Definition     |
|-----|------------------|-----|----------------|
| 1   | TRD0[0]P         | 2   | TRD0[0]N       |
| 3   | TRD0[1]P         | 4   | TRD0[1]N       |
| 5   | NA               | 6   | NC             |
| 7   | TRD0[2]P         | 8   | TRD0[2]N       |
| 9   | TRD0[3]P         | 10  | TRD0[3]N       |
| 11  | PHY0_LED_10/100n | 12  | PHY0_LED_1000n |
| 13  | PHY0_LED_ACTn    | 14  | VCC3           |

**J7: INTERNAL USB CON**

| Pin | Definition  | Pin | Definition |
|-----|-------------|-----|------------|
| 1   | P5V_USB_P34 | 2   | USBCON_3N  |
| 3   | USBCON_3P   | 4   | USBCON_4N  |
| 5   | USBCON_4P   | 6   | GND        |

**SW4: RESET BUTTON**

| Pin | Definition | Pin | Definition |
|-----|------------|-----|------------|
| 1   | GND        | 2   | RESET      |

**JP7: PMIC VBACKUP Select**

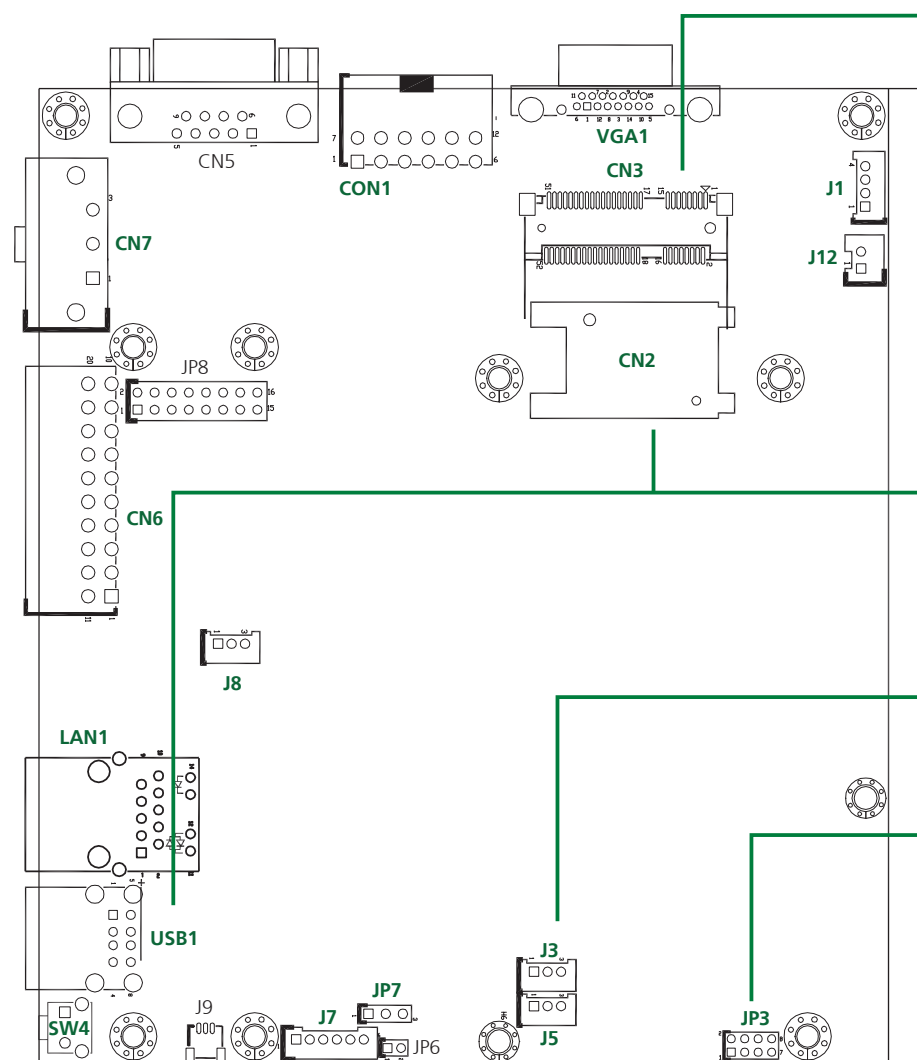
| Pin No. | Status | Function Description |
|---------|--------|----------------------|
| 1-2 (*) | Short  | Normal               |
| 2-3     | Short  | Clear PMIC registers |

Please set jumper in the marked position of JP7 to enable the cell battery

Quick reference guide is subject to change without prior notice. For any update, please visit our website: [www.nexcom.com](http://www.nexcom.com) □ =Pin1 [\*]=Default setting

# NISB 60 Quick Reference Guide

Ver A. (P/N: 60177A0316X00)



**CN3: MINI-PCIE CON**

| Pin | Definition          | Pin | Definition    | Pin | Definition          | Pin | Definition |
|-----|---------------------|-----|---------------|-----|---------------------|-----|------------|
| 1   | PCIE_WAKE#          | 2   | 3VSB_MINI1    | 27  | GND                 | 28  | P1V5_MINI1 |
| 3   | NC                  | 4   | GND           | 29  | GND                 | 30  | SMB_CLK    |
| 5   | NC                  | 6   | P1V5_MINI1    | 31  | PCIE_TXN7_MINI_PCIE | 32  | SMB_DATA   |
| 7   | MINICARD1CLKREQ#    | 8   | NC            | 33  | PCIE_TXP7_MINI_PCIE | 34  | GND        |
| 9   | GND                 | 10  | NC            | 35  | GND                 | 36  | USB_10N    |
| 11  | REFCLK-             | 12  | NC            | 37  | GND                 | 38  | USB_10P    |
| 13  | REFCLK+             | 14  | NC            | 39  | 3VSB_MINI1          | 40  | GND        |
| 15  | GND                 | 16  | NC            | 41  | 3VSB_MINI1          | 42  | NC         |
| 17  | NC                  | 18  | GND           | 43  | GND                 | 44  | NC         |
| 19  | NC                  | 20  | MINICARD1DIS# | 45  | NC                  | 46  | NC         |
| 21  | GND                 | 22  | PLT_RST_PCIE# | 47  | NC                  | 48  | P1V5_MINI1 |
| 23  | PCIE_RXN7_MINI_PCIE | 24  | 3VSB_MINI1    | 49  | NC                  | 50  | GND        |
| 25  | PCIE_RXP7_MINI_PCIE | 26  | GND           | 51  | NC                  | 52  | 3VSB_MINI1 |

**USB1: USB CON**

| Pin | Definition | Pin | Definition |
|-----|------------|-----|------------|
| 1   | VCC        | 2   | DATA1-     |
| 3   | DATA1+     | 4   | GND        |
| 5   | VCC        | 6   | DATA-      |
| 7   | DATA+      | 8   | GND        |

**CN2: SIM CARD CON**

| Pin | Definition | Pin | Definition |
|-----|------------|-----|------------|
| 1   | UIM_PWR    | 2   | UIM_RESET  |
| 3   | UIM_CLK    |     |            |
| 5   | GND        | 6   | UIM_VPP    |
| 7   | UIM_DATA   |     |            |

**J3: MIC-IN CON**

| Pin | Definition | Pin | Definition |
|-----|------------|-----|------------|
| 1   | MIC1_R3    | 2   | GND        |
| 3   | MIC1_L3    |     |            |

**J5: LINE-OUT CON**

| Pin | Definition | Pin | Definition |
|-----|------------|-----|------------|
| 1   | LOUT_R3    | 2   | GND        |
| 3   | LOUT_L3    |     |            |

**JP3: JTAG CON**

| Pin | Definition | Pin | Definition    |
|-----|------------|-----|---------------|
| 1   | JTAG_TRSTn | 2   | JTAG_TMS      |
| 3   | JTAG_TCK   | 4   | N/A           |
| 5   | JTAG_TDI   | 6   | VDDSHV6(3.3V) |
| 7   | JTAG_TDO   | 8   | GND           |

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